

Dual N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY

$V_{DSS}$	30V
$R_{DS(on)(MAX)}$	0.016 $\Omega$
$I_D$	35A

FEATURES

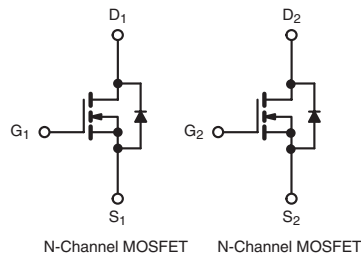
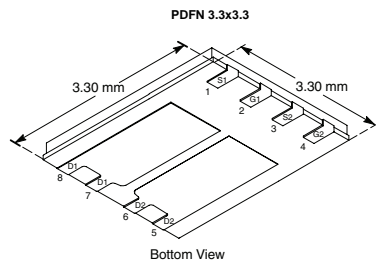
- TrenchFET<sup>®</sup> Power MOSFET
- 100 %  $R_g$  Tested
- 100 UIS Tested



RoHS  
COMPLIANT  
HALOGEN  
FREE

APPLICATIONS

- Synchronous Rectification
- Notebook System Power
- POL
- Low Current DC/DC



Absolute Maximum Ratings ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current@10V	$I_D$	$T_C = 25\text{ }^\circ\text{C}$	35
		$T_C = 100\text{ }^\circ\text{C}$	20
Pulsed Drain Current	$I_{DM}$	140	A
Single Pulse Avalanche Energy	$E_{AS}$	$L = 0.1\text{ mH}$	13
Avalanche Current			$I_{AS}$
Total Power Dissipation	$P_D$	$T_C = 25\text{ }^\circ\text{C}$	27
		$T_A = 25\text{ }^\circ\text{C}$	0.22
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	LIMIT.	Unit
Thermal resistance, junction-to-ambient	$R_{\theta JA}$	4.6	$^\circ\text{C}/\text{W}$
Thermal resistance, junction-to-case	$R_{\theta JC}$	62	

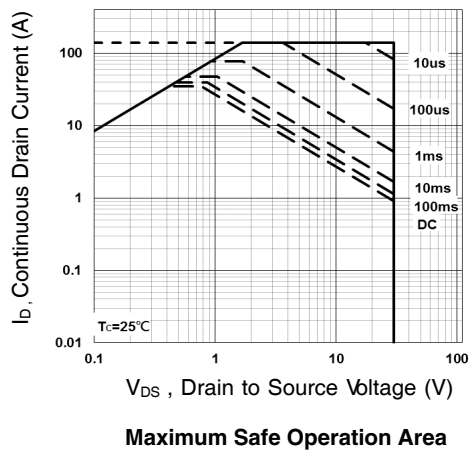
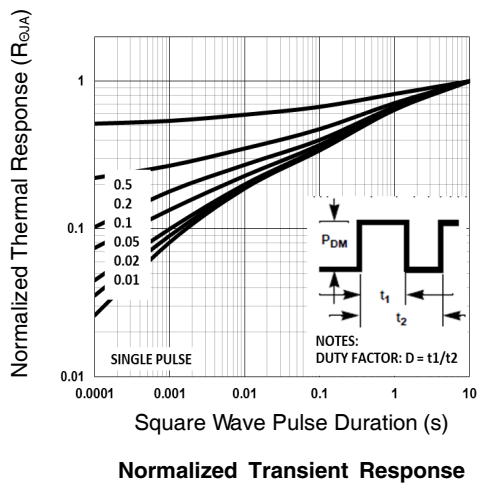
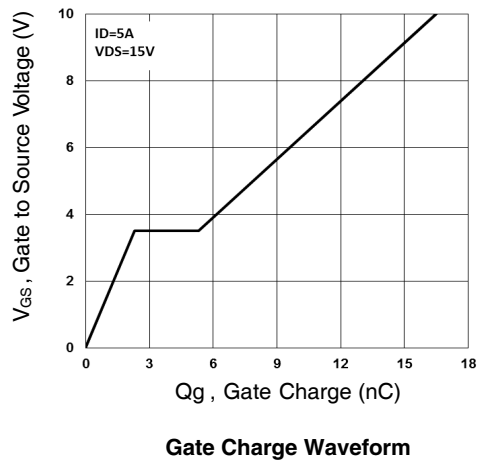
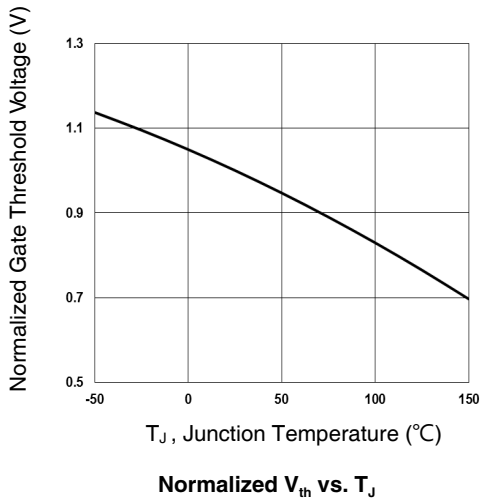
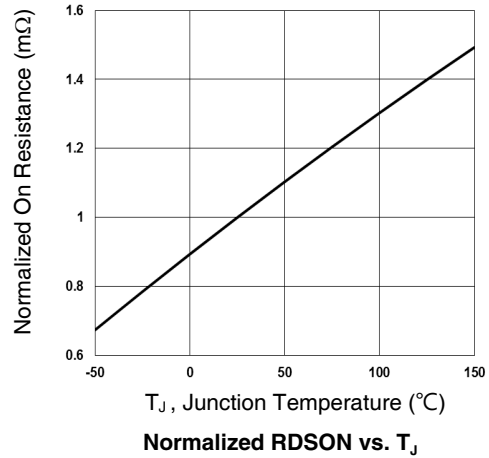
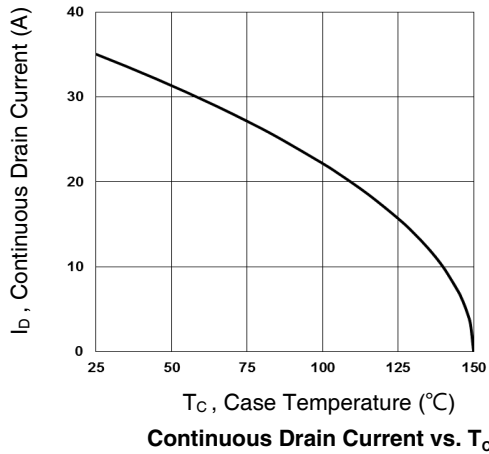
**Electrical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
<b>Static Characteristics</b>							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	30	-	-	V	
Gate-body Leakage current	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$	$T_J = 25^\circ\text{C}$	-	-	1	$\mu\text{A}$
			$T_J = 125^\circ\text{C}$	-	-	10	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	1		2.5	V	
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	-	0.012	0.016	$\Omega$	
		$V_{GS} = 4.5\text{ V}, I_D = 5\text{ A}$	-	0.018	0.024		
Forward Transconductance	$g_{fs}$	$V_{DS} = 10\text{ V}, I_D = 3\text{ A}$	-	6	-	S	
<b>Dynamic Characteristics</b>							
Input Capacitance	$C_{iss}$	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	-	622	-	$\mu\text{F}$	
Output Capacitance	$C_{oss}$		-	87	-		
Reverse Transfer Capacitance	$C_{rss}$		-	62	-		
<b>Switching Characteristics</b>							
Total Gate Charge	$Q_g$	$V_{GS} = 4.5\text{ V}, V_{DS} = 15\text{ V}, I_D = 5\text{ A}$	-	7.6	-	nC	
Gate-Source Charge	$Q_{gs}$		-	2.5	-		
Gate-Drain Charge	$Q_{gd}$		-	5	-		
Turn-On Delay Time	$t_d(on)$	$V_{DD} = 15\text{ V}, I_D = 5\text{ A}, V_{GEN} = 4.5\text{ V},$ $R_G = 6\ \Omega$	-	4	-	nS	
Rise Time	$t_r$		-	12	-		
Turn-Off Delay Time	$t_d(off)$		-	24	-		
Fall Time	$t_f$		-	6.8	-		
<b>Drain-Source Body Diode Characteristics</b>							
Diode Forward Voltage	$V_{SD}$	$I_S = -1\text{ A}, V_{GS} = 0\text{ V}$	-	-	1	V	
Continuous Source-Drain Diode Current	$I_S$	$T_J = 25^\circ\text{C}$	-	-	35	A	
Continuous Source Current	$I_{SM}$		-	-	70	A	

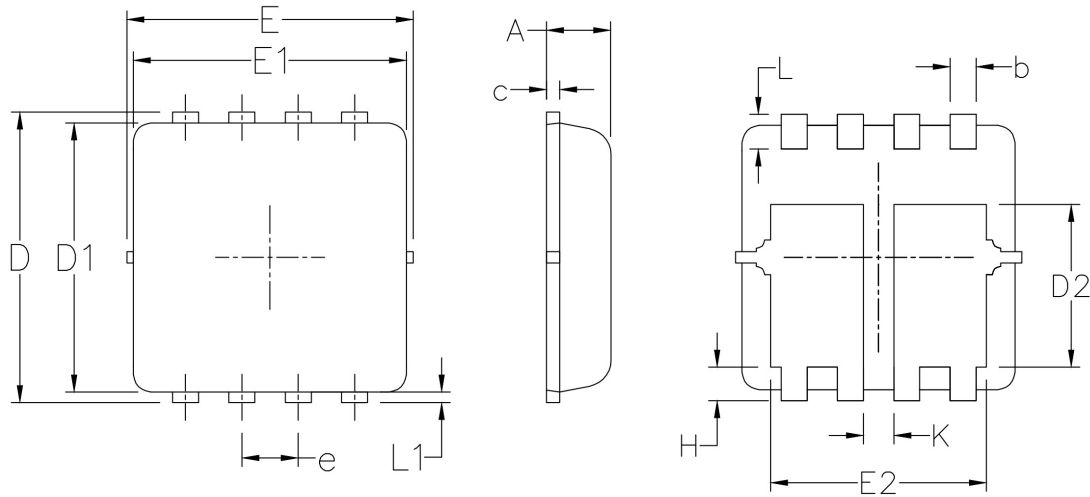
**Notes:**

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.1\text{mH}, I_{AS}=16\text{A}, R_G=25\ \Omega, \text{Starting } T_J=25^\circ\text{C}.$
- The data tested by pulsed , pulse width  $\leq 300\ \mu\text{s}$  , duty cycle  $\leq 2\%$ .
- Essentially independent of operating temperature.

**TYPICAL CHARACTERISTICS** (25 °C unless noted)



PDFN3333-8B\_Dual\_P PACKAGE OUTLIN



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
E	3.00	3.20	3.40
E1	3.00	3.10	3.20
E2	2.39	2.49	2.59
e	0.65 BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	--	0.13	--
K	0.30	--	--

Notes:

1. Refer to JEDEC MO-240 variation CA.
2. Dimensions "D1" and "E1" do NOT include mold flash protrusions or gate burrs.
3. Dimensions "D1" and "E1" include interterminal flash or protrusion. Interterminal flash or protrusion shall not exceed 0.25mm per side.