

## N-Channel 60 V (D-S) MOSFET

### PRODUCT SUMMARY

BV <sub>DSS</sub>	60V
R <sub>DS(on)(MAX.)</sub>	0.003Ω
I <sub>D</sub>	120A

### FEATURES

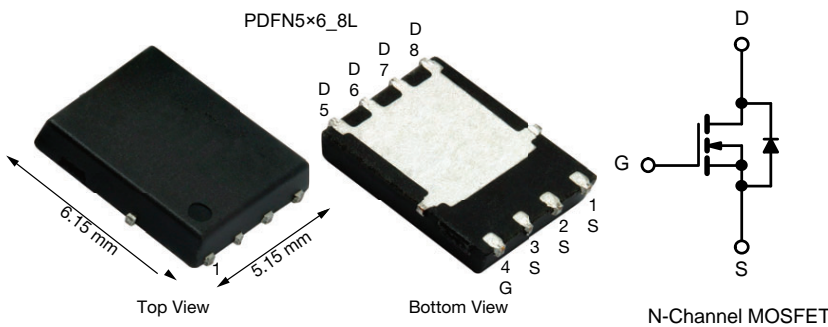
- SGT technology Power MOSFET
- Material categorization:



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- Power management functions
- Synchronous Rectifier
- DC/DC Converter



### Absolute Maximum Ratings (T<sub>A</sub> = 25 °C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current@10V	I <sub>D</sub>	T <sub>C</sub> = 25 °C	120
		T <sub>C</sub> = 100 °C	79
Pulsed Drain Current	I <sub>DM</sub>	500	A
Single Pulse Avalanche Energy	E <sub>AS</sub>	352.8	mJ
Avalanche Current	I <sub>AS</sub>	40	A
Total Power Dissipation	P <sub>D</sub>	96	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### Thermal Characteristics

Parameter	Symbol	TYP.	MAX.	Unit
Thermal resistance, junction-to-ambient	R <sub>θJA</sub>	-	48	°C / W
Thermal resistance, junction-to-case	R <sub>θJC</sub>	-	1.3	

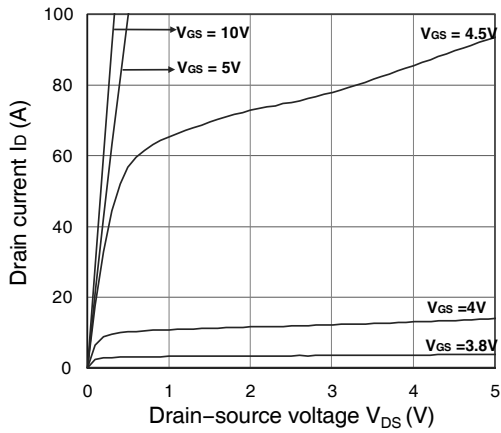
**Electrical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}$ , $I_D = 250\ \mu\text{A}$	60	-	-	V
Gate-body Leakage current	$I_{GSS}$	$V_{DS} = 0\text{ V}$ , $V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 60\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 25^\circ\text{C}$	-	-	1	$\mu\text{A}$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250\ \mu\text{A}$	2		4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$ , $I_D = 19\text{ A}$	-	0.0025	0.003	$\Omega$
Forward Transconductance	$g_{fs}$	$V_{DS} = 10\text{ V}$ , $I_D = 19\text{ A}$	-	89	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 30\text{ V}$ , $V_{GS} = 0\text{ V}$ , $f = 1\text{ MHz}$	-	4072	-	pF
Output Capacitance	$C_{oss}$		-	1055	-	
Reverse Transfer Capacitance	$C_{rss}$		-	33	-	
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 30\text{ V}$ , $V_{GS} = 10\text{ V}$ , $I_D = 50\text{ A}$	-	70	-	nC
Gate-Source Charge	$Q_{gs}$		-	18	-	
Gate-Drain Charge	$Q_{gd}$		-	20.7	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}$ , $I_D \cong 19\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_G = 3\ \Omega$	-	19	-	nS
Rise Time	$t_r$		-	18	-	
Turn-Off Delay Time	$t_{d(off)}$		-	42	-	
Fall Time	$t_f$		-	23	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage	$V_{SD}$	$I_S = 19\text{ A}$ , $V_{GS} = 0\text{ V}$	-	-	1.2	V
Continuous Source-Drain Diode Current	$I_S$	$T_J = 25^\circ\text{C}$	-	-	120	A
Continuous Source Current	$I_{SM}$		-	-	240	A
Reverse Recovery Charge	$Q_{rr}$	$T_J = 25^\circ\text{C}$ , $I_F = 19\text{ A}$ , $di/dt = 100\text{ A}/\mu\text{s}$	-	10	-	nC
Reverse Recovery Time	$t_{rr}$		-	45	100	ns

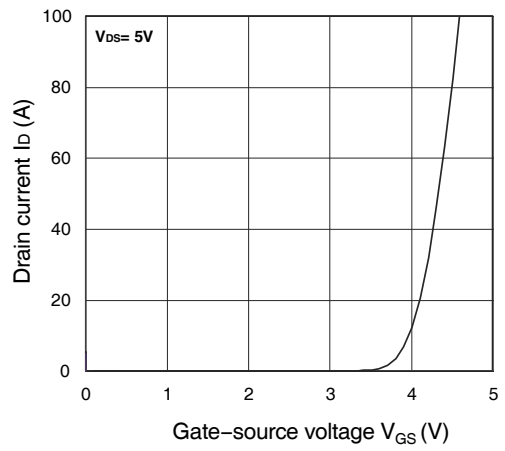
**Notes:**

- Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .
- The EAS data shows Max. rating . The test condition is  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.4\text{mH}$ ,  $I_{AS}=40\text{A}$ .
- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$ .
- This value is guaranteed by design hence it is not included in the production test.

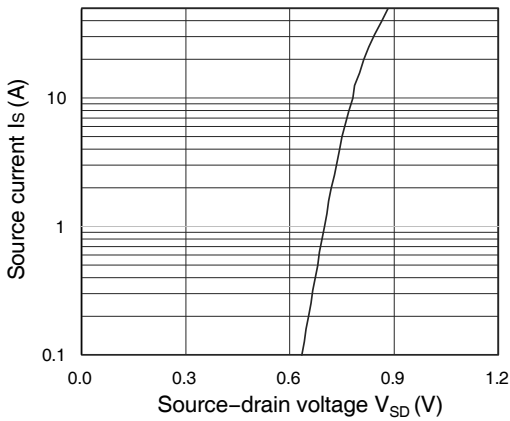
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



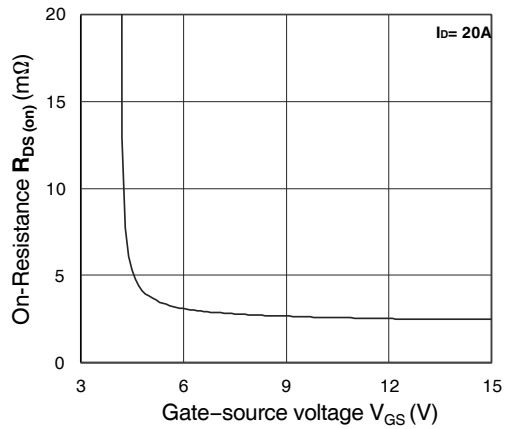
**Output Characteristics**



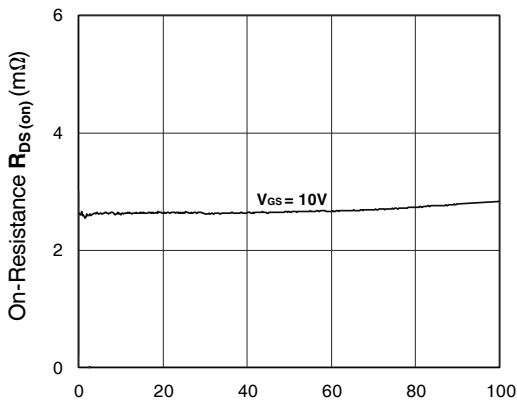
**Transfer Characteristics**



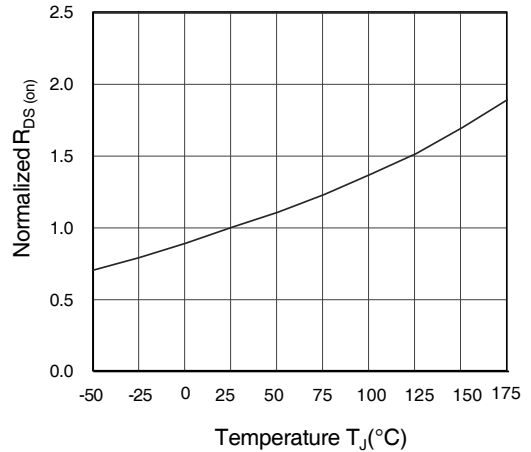
**Forward Characteristics of Reverse**



**$R_{DS(ON)}$  vs.  $V_{GS}$**

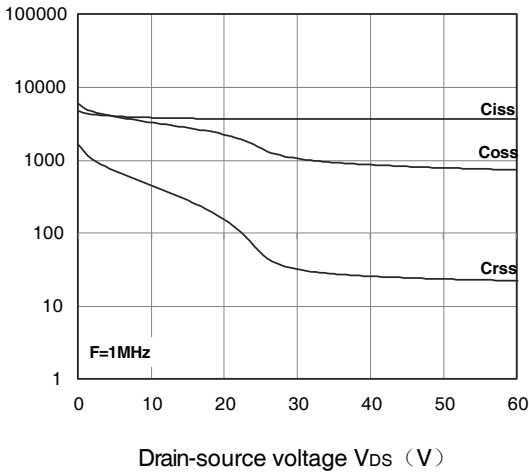


**$R_{DS(ON)}$  vs.  $I_D$**

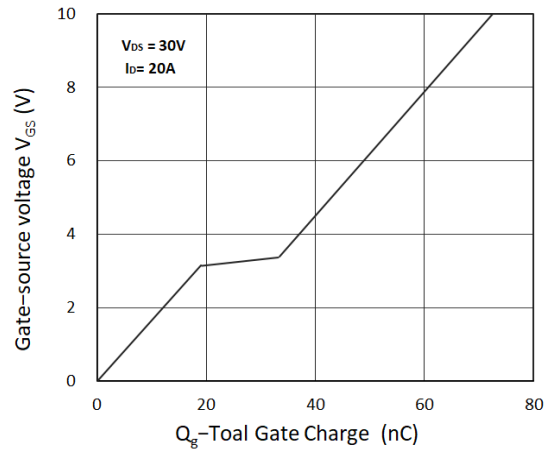


**Normalized  $R_{DS(ON)}$  vs. Temperature**

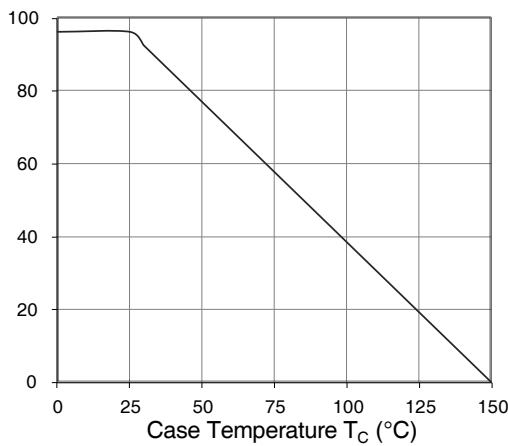
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



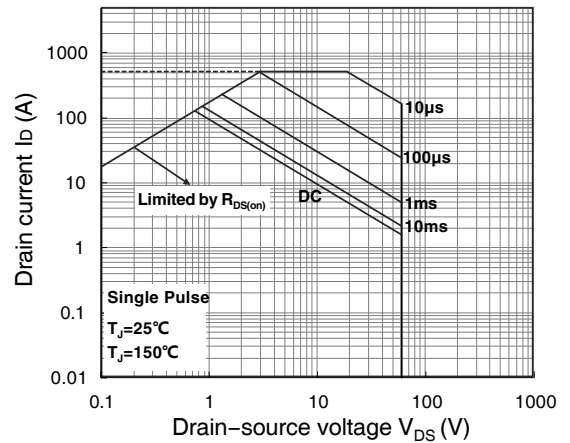
**Capacitance Characteristics**



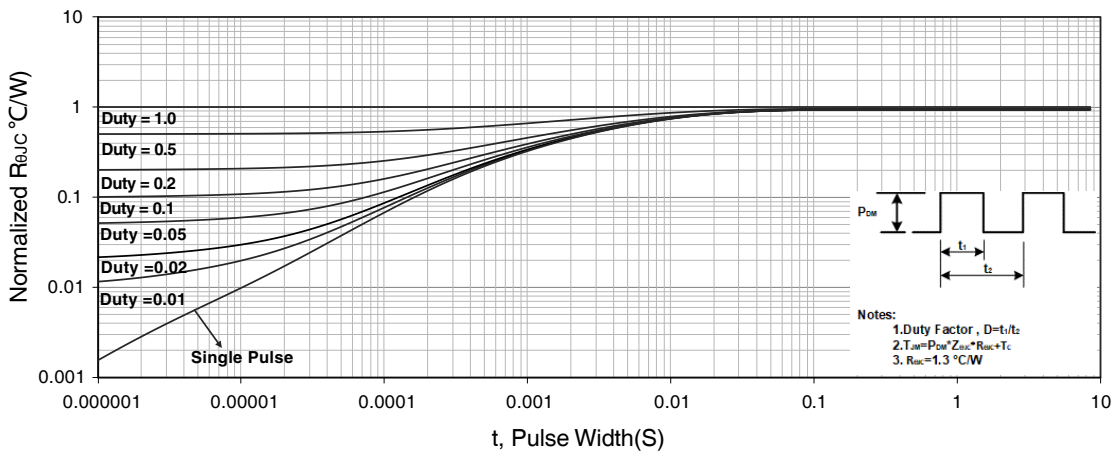
**Gate Charge Characteristics**



**Power Dissipation**



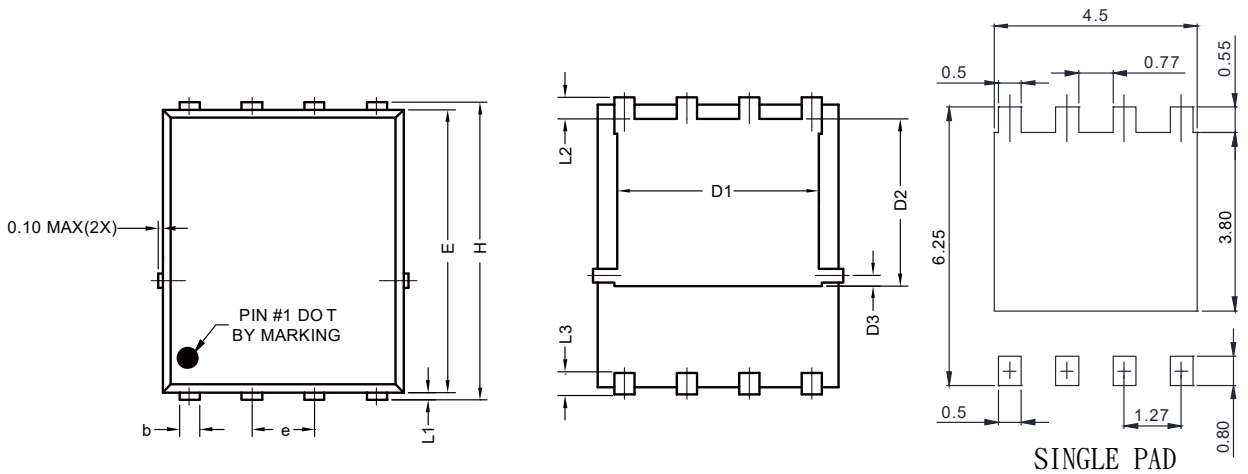
**Safe Operating Area**



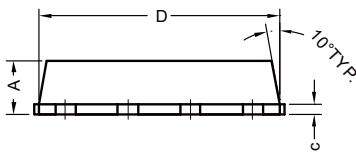
**Normalized Maximum Transient Thermal**

**Impedance**

PDFN5x6-8L\_EP1\_P PACKGE OUTLIN



RECOMMENDED LAND PATTERN



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.800	1.170	0.031	0.046
b	0.340	0.490	0.013	0.019
c	0.20	0.34	0.008	0.013
D	4.800	5.100	0.009	0.011
D1	3.800	4.200	0.150	0.165
D2	3.180	3.78	0.125	0.149
D3	0.150	0.360	0.006	0.142
E	5.650	5.900	0.222	0.232
e	1.270 TYP		0.050 TYP	
H	5.900	6.150	0.232	0.242
L1	0.050	0.250	0.002	0.010
L2	0.380	0.620	0.015	0.024
L3	0.380	0.75	0.015	0.030